



Attorney Docket No.: 99P7613/109001

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1/28/02

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Singh, et al.  
Serial No. : 09/320,833  
Filed : May 26, 1999  
Title : PREFETCH STREAMING BUFFER

Art Unit : 2183  
Examiner : Patel, G.

(N.G.)

Commissioner for Patents  
Washington, D.C. 20231

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Technology Center 2100

RESPONSE

In response to the action mailed May 24, 2001, please amend the application as follows:

In the Title:

Please replace the Title with the following:

--APPARATUS AND METHOD FOR ALIGNING VARIABLE-WIDTH  
INSTRUCTIONS WITH A PREFETCH BUFFER--.

In the claims:

Please cancel claims 9-13.

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Please amend claims 1-5 and 7-8 as follows:

1. (Amended) Method for providing a plurality of aligned instructions from an instruction stream having variable-width instructions provided by a memory unit for execution within a pipelined microprocessor comprising a prefetch buffer, said method comprising the steps of:

for each instruction cycle in which the microprocessor issues one or more instructions,  
-in case said prefetch buffer contains invalid data:

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

November 26, 2001

Date of Deposit

Matilde C. Sanchez  
Signature

Matilde C. Sanchez

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